

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Soo-seong Kim et al.

Application No.: 10/807,917

Filed: March 23, 2004

For: MOS-GATED TRANSISTOR
WITH IMPROVED UIS CAPABILITY

Customer No.: 20350

Confirmation No. 4012

Examiner: Monica Lewis

Technology Center/Art Unit: 2822

AMENDMENT SUBMITTED WITH RCE

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 19, 2008

Sir:

In response to the Final Office Action mailed August 21, 2007 on the above-referenced application, please enter the following amendments and remarks. A Petition for Extension of Time accompanies this response.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 4 of this paper and include an annotated sheet showing changes.

A **replacement drawing sheet** with amended drawing Fig. 2C is attached following page 8 of this paper.